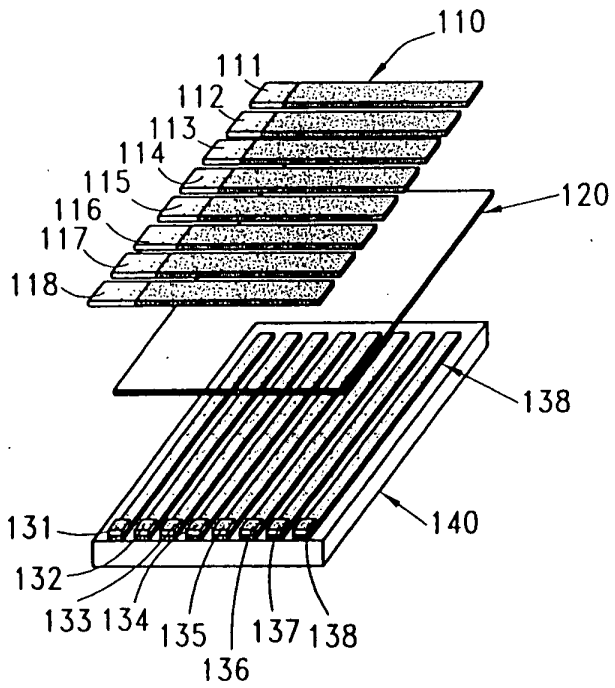
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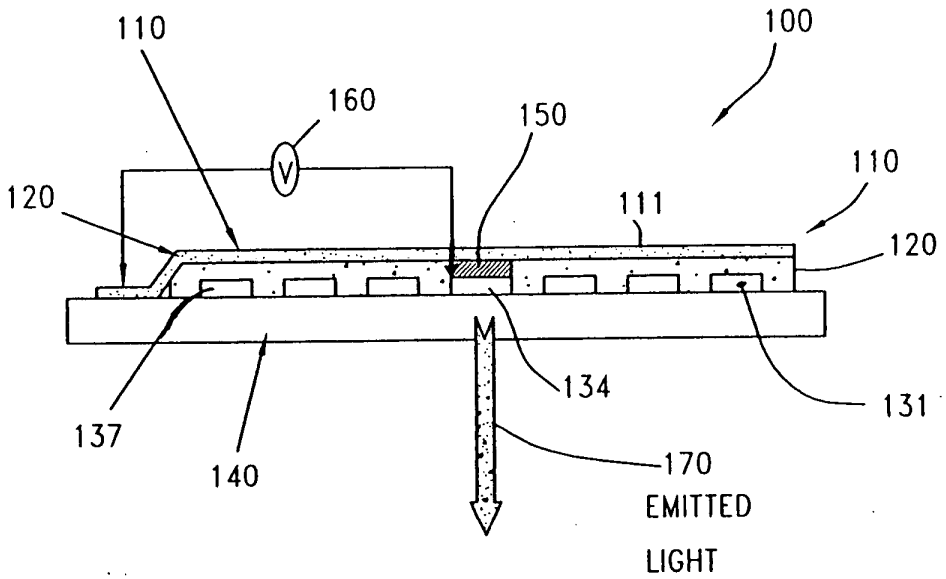
PRIOR ART

FIG. 1A



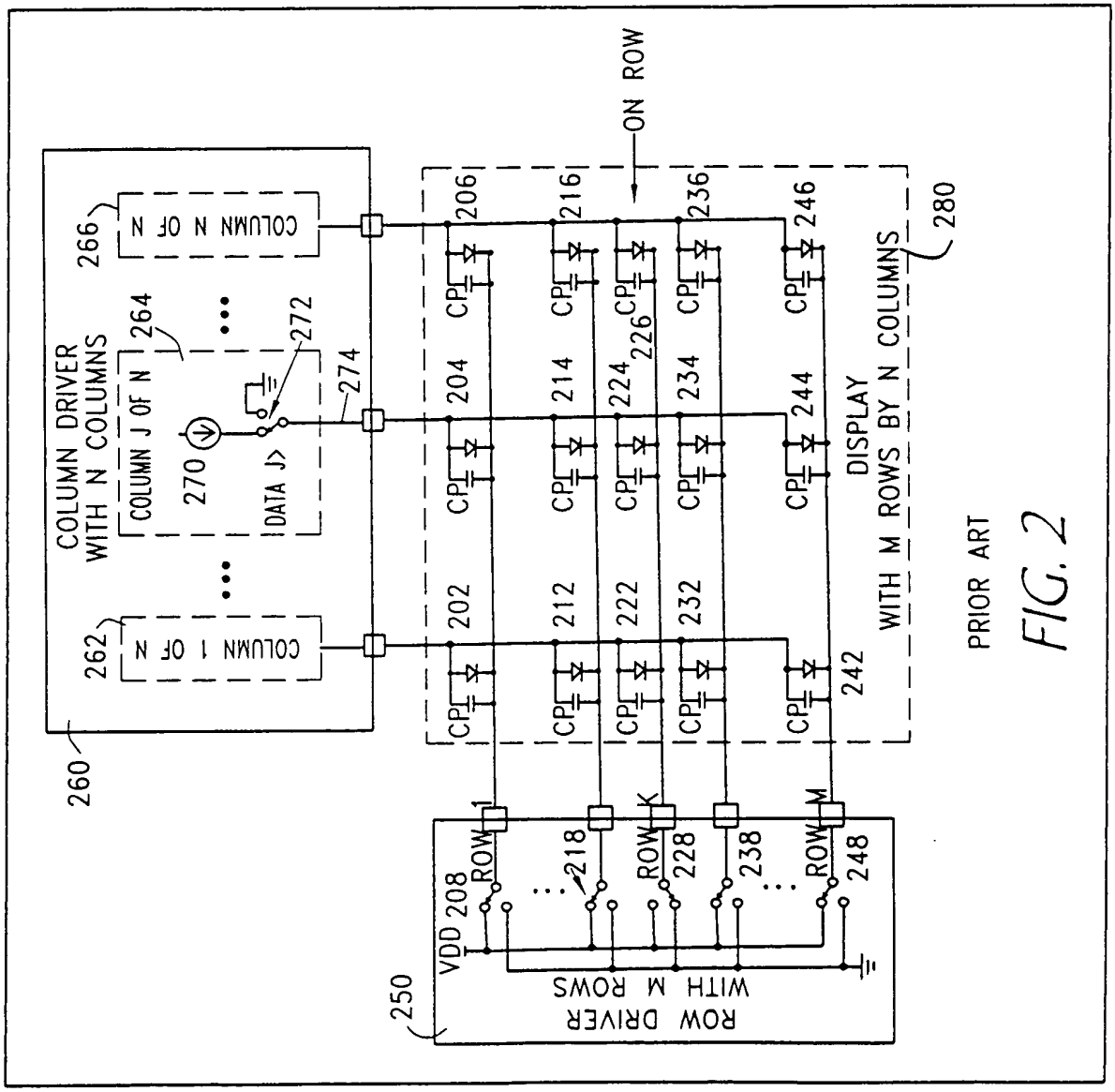

PRIOR ART

FIG. 1B




Title: SYSTEM FOR PROVIDING A PULSE AMPLITUDE MODULATION FOR OLED DISPLAY DRIVERS
Inventor(s): James W. Everitt
Atty Docket: CLMCR.016A1

Title: SYSTEM FOR PROVIDING A
PULSE AMPLITUDE MODULATION
OR OLED DISPLAY DRIVERS
Inventor(s): James W. Everitt
Atty Docket: CLMCR.016A1



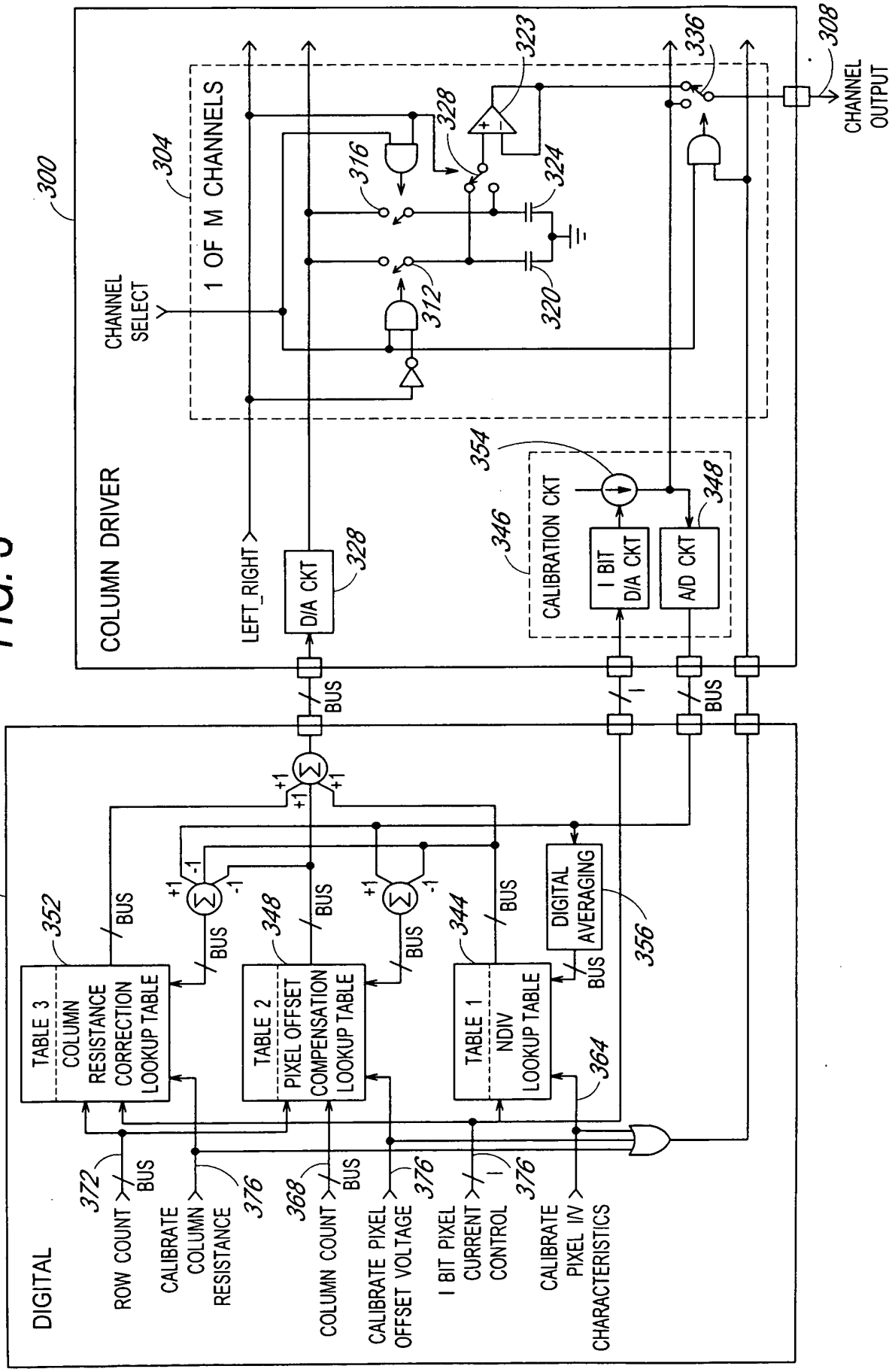

PRIOR ART

FIG. 2

Title: SYSTEM FOR PROVIDING A PULSE AMPLITUDE MODULATION FOR OLED DISPLAY DRIVERS
Inventor(s): James W. Everitt
Atty Docket: CLMCR.016A1

Docket # 20060207

FIG. 3



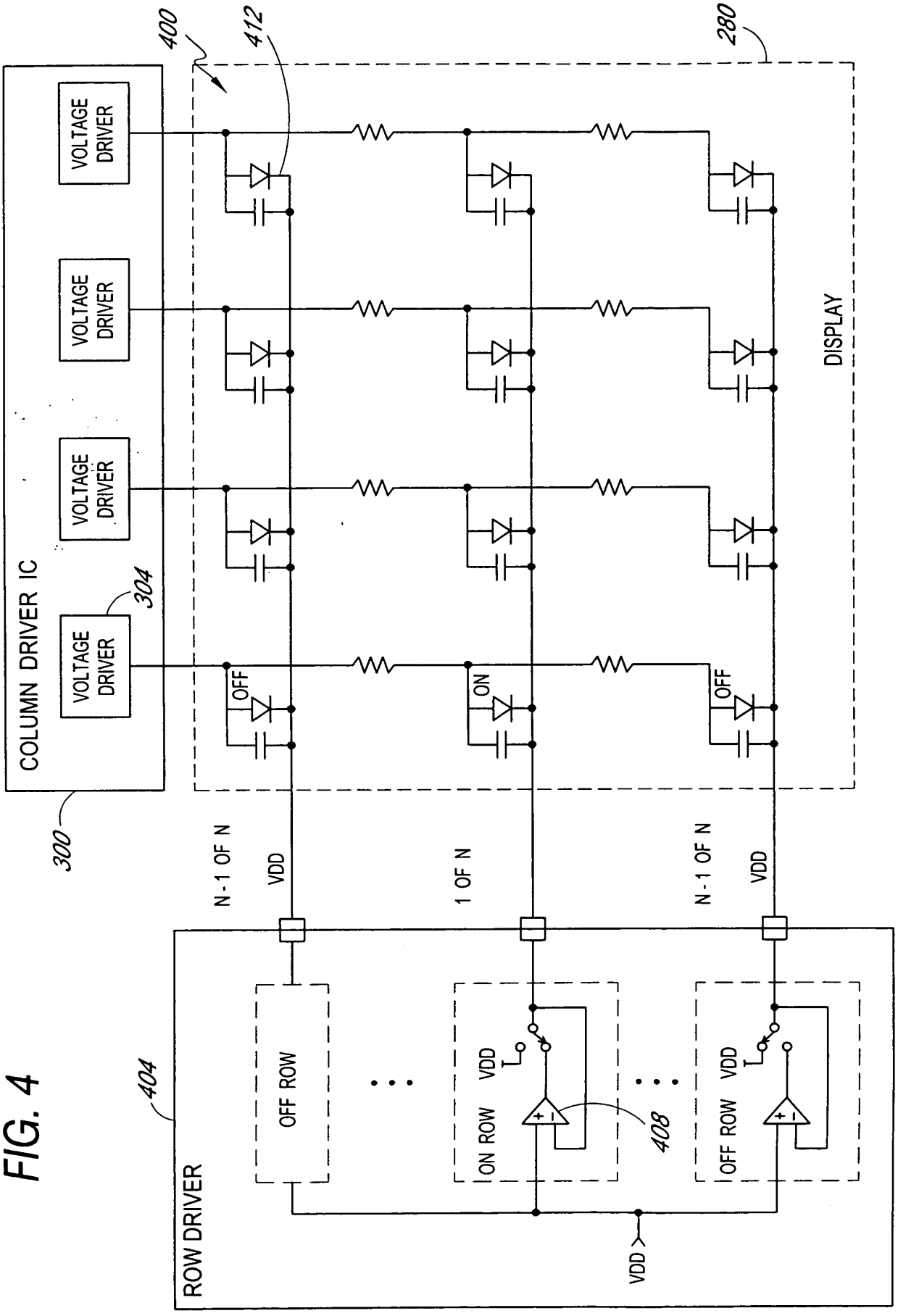

400
412
280
DISPLAY
COLUMN DRIVER IC
VOLTAGE DRIVER
VOLTAGE DRIVER
VOLTAGE DRIVER
VOLTAGE DRIVER
304
300
OFF
ON
OFF
N-1 OF N
VDD
1 OF N
N-1 OF N
VDD
404
ROW DRIVER
OFF ROW
ON ROW
VDD
408
OFF ROW
VDD
VDD

FIG. 4

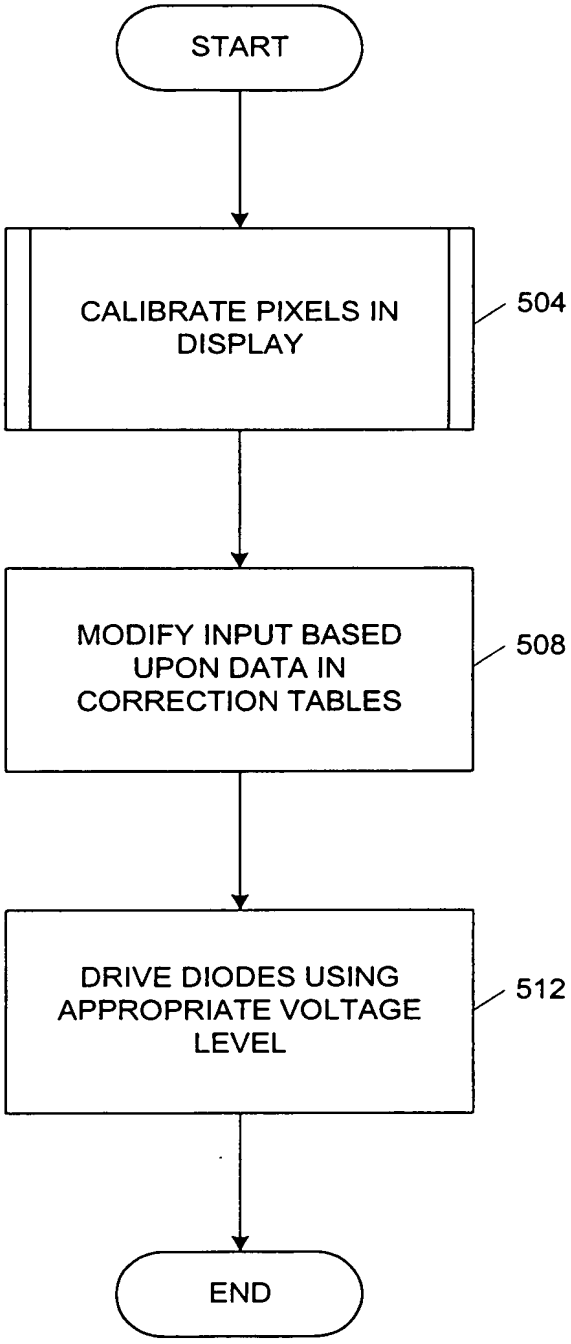

START
CALIBRATE PIXELS IN DISPLAY
504
MODIFY INPUT BASED UPON DATA IN CORRECTION TABLES
508
DRIVE DIODES USING APPROPRIATE VOLTAGE LEVEL
512
END

FIG. 5

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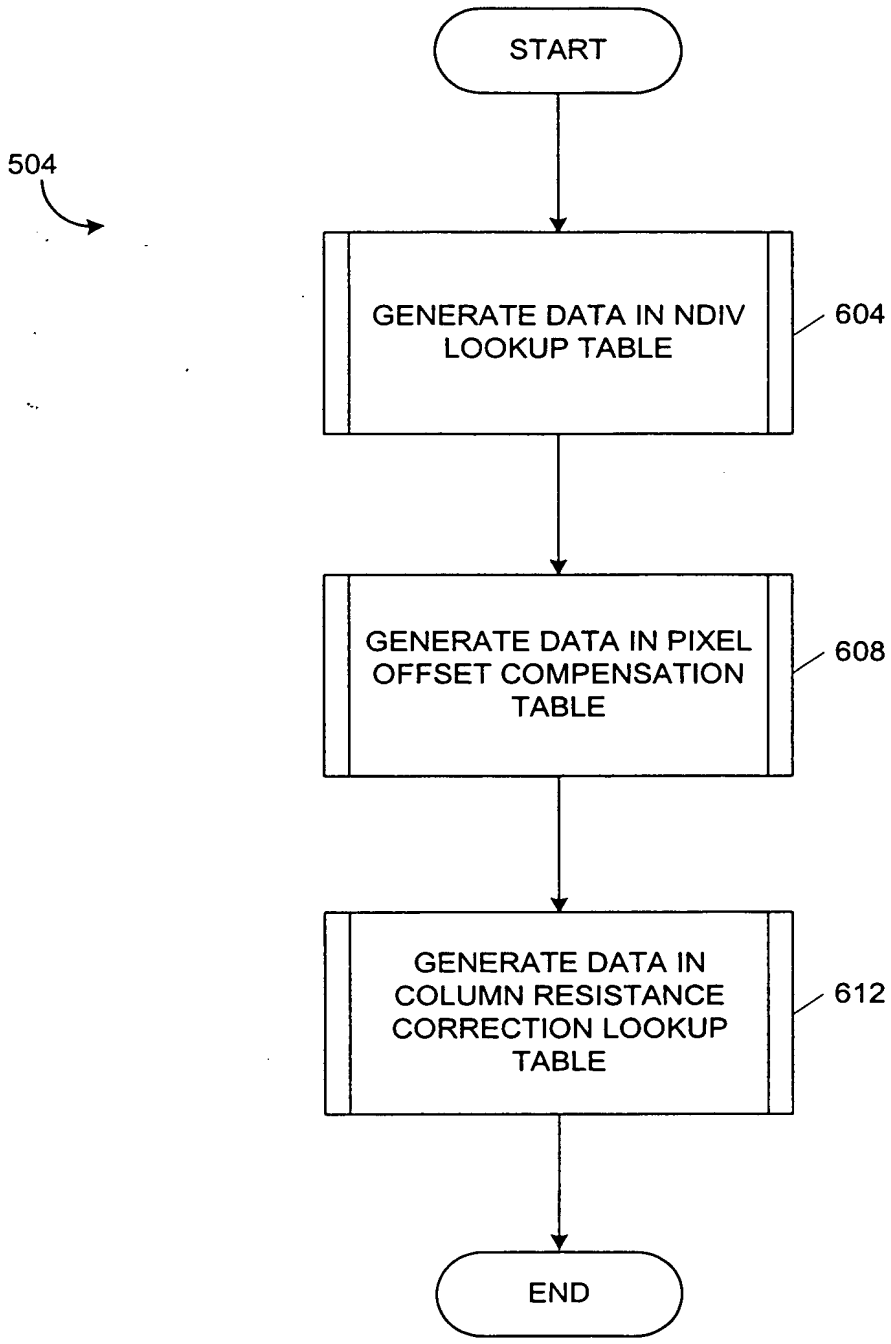



*FIG. 6*

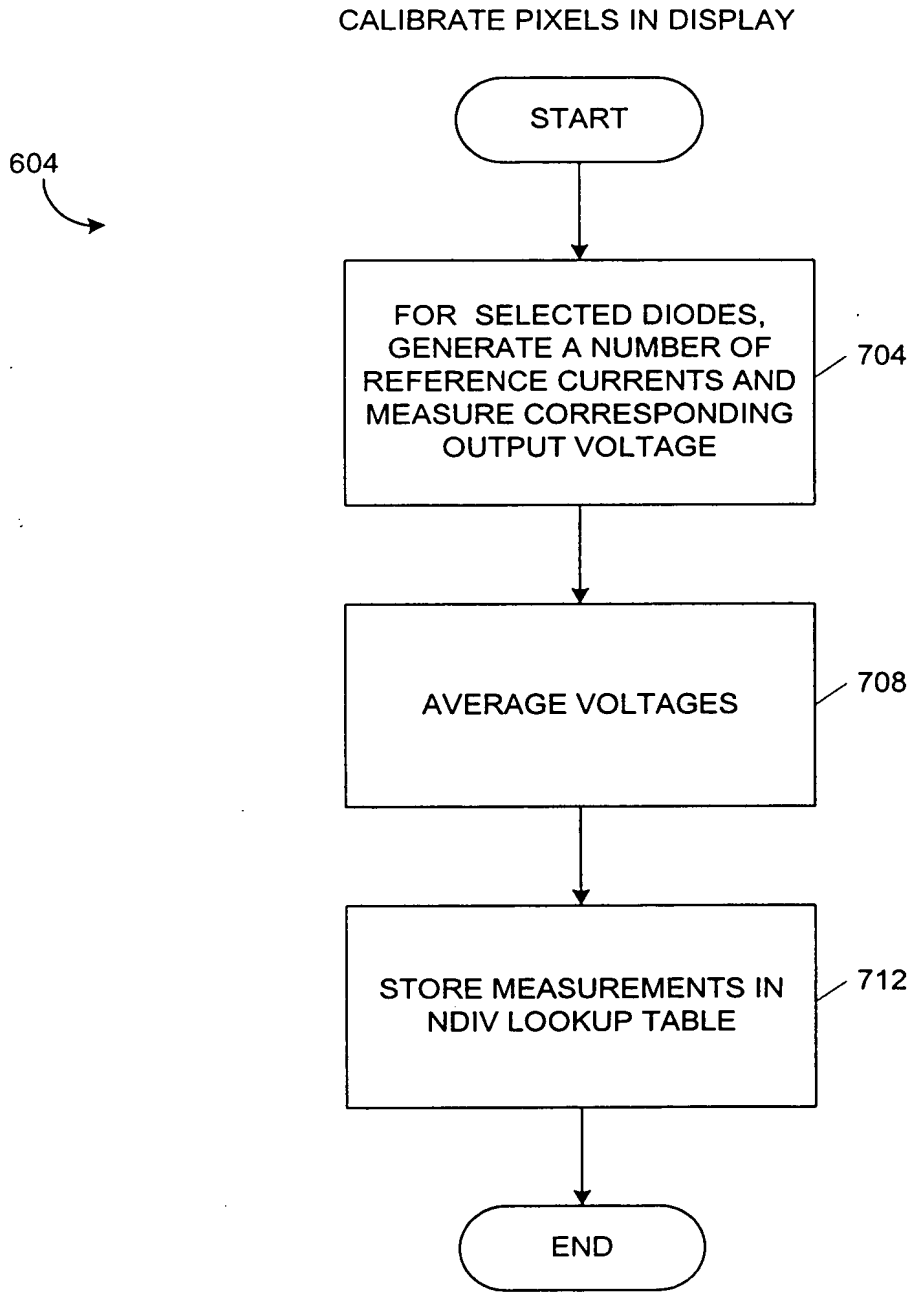

CALIBRATE PIXELS IN DISPLAY
604
START
FOR SELECTED DIODES, GENERATE A NUMBER OF REFERENCE CURRENTS AND MEASURE CORRESPONDING OUTPUT VOLTAGE
704
AVERAGE VOLTAGES
708
STORE MEASUREMENTS IN NDIV LOOKUP TABLE
712
END

FIG. 7

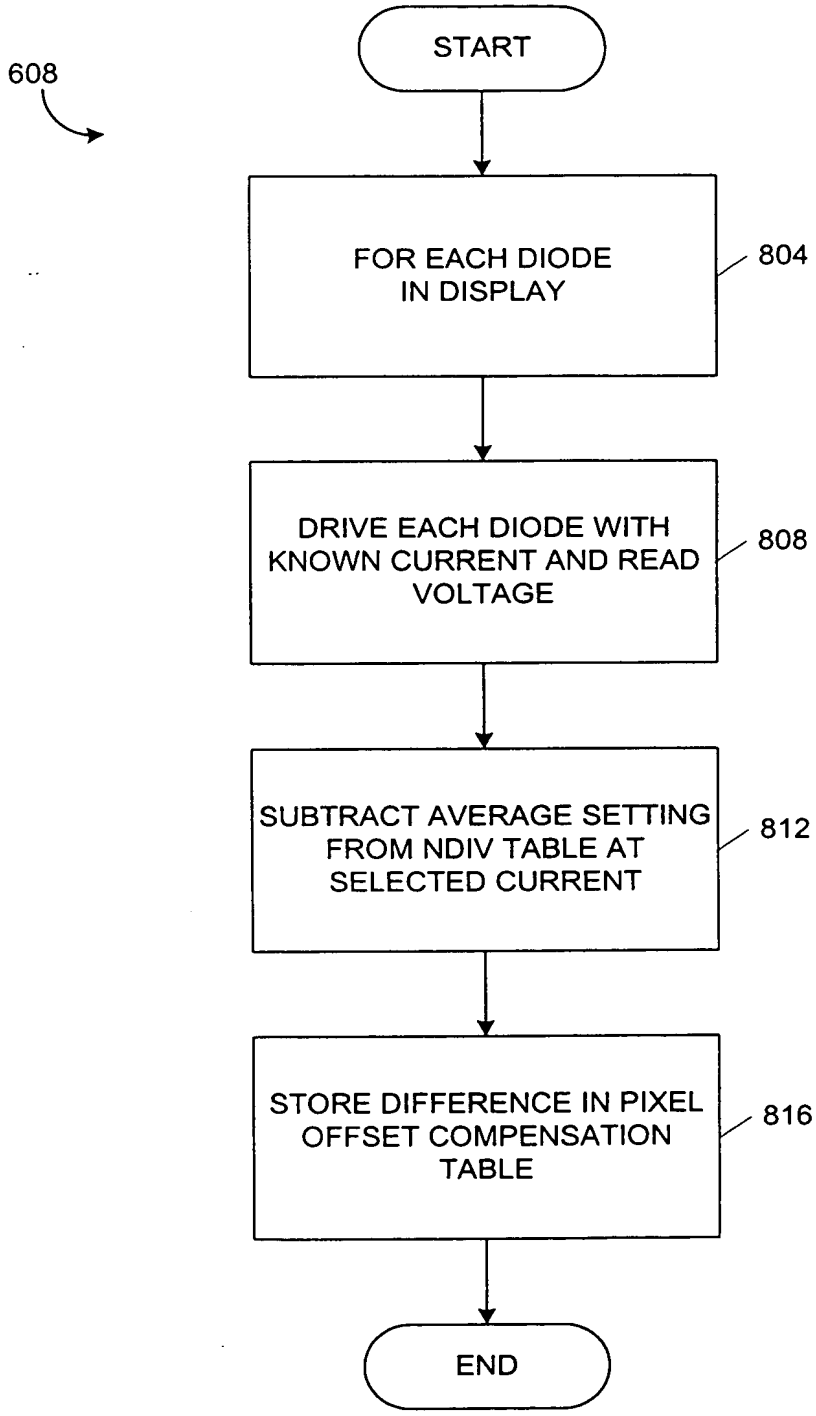

608
START
FOR EACH DIODE IN DISPLAY
804
DRIVE EACH DIODE WITH KNOWN CURRENT AND READ VOLTAGE
808
SUBTRACT AVERAGE SETTING FROM NDIV TABLE AT SELECTED CURRENT
812
STORE DIFFERENCE IN PIXEL OFFSET COMPENSATION TABLE
816
END

FIG. 8

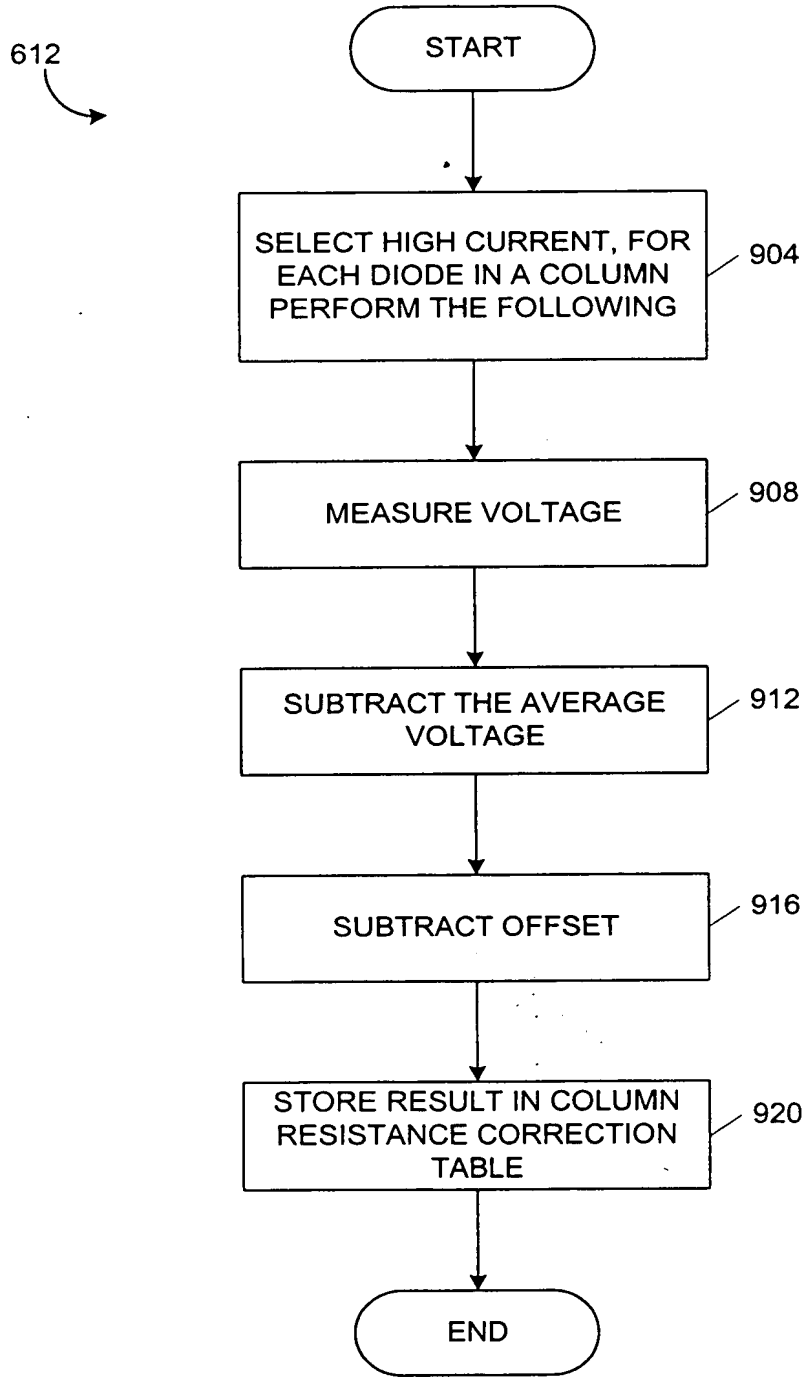

612
START
SELECT HIGH CURRENT, FOR EACH DIODE IN A COLUMN PERFORM THE FOLLOWING
904
MEASURE VOLTAGE
908
SUBTRACT THE AVERAGE VOLTAGE
912
SUBTRACT OFFSET
916
STORE RESULT IN COLUMN RESISTANCE CORRECTION TABLE
920
END

FIG. 9